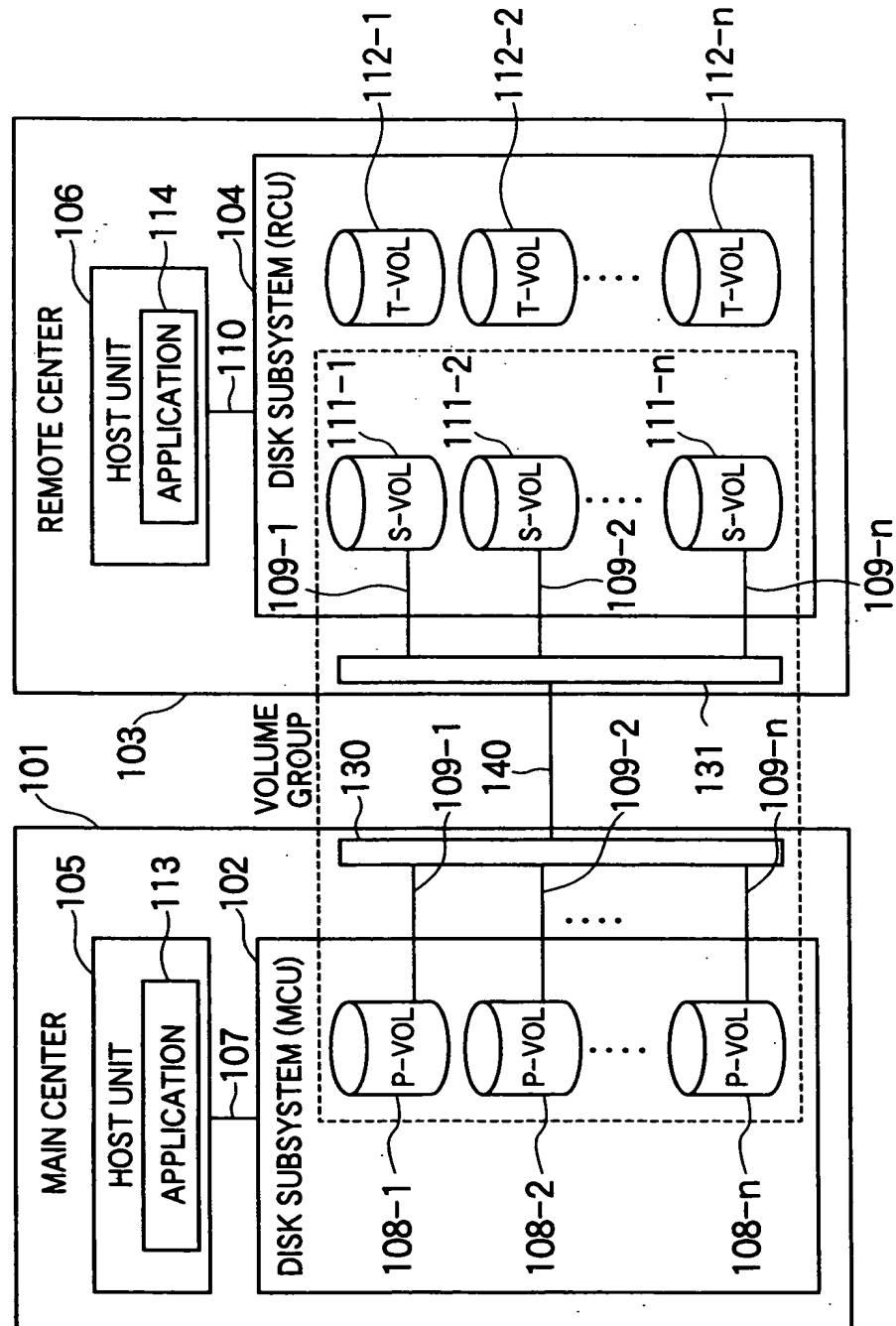


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FIG. 1



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FIG. 2

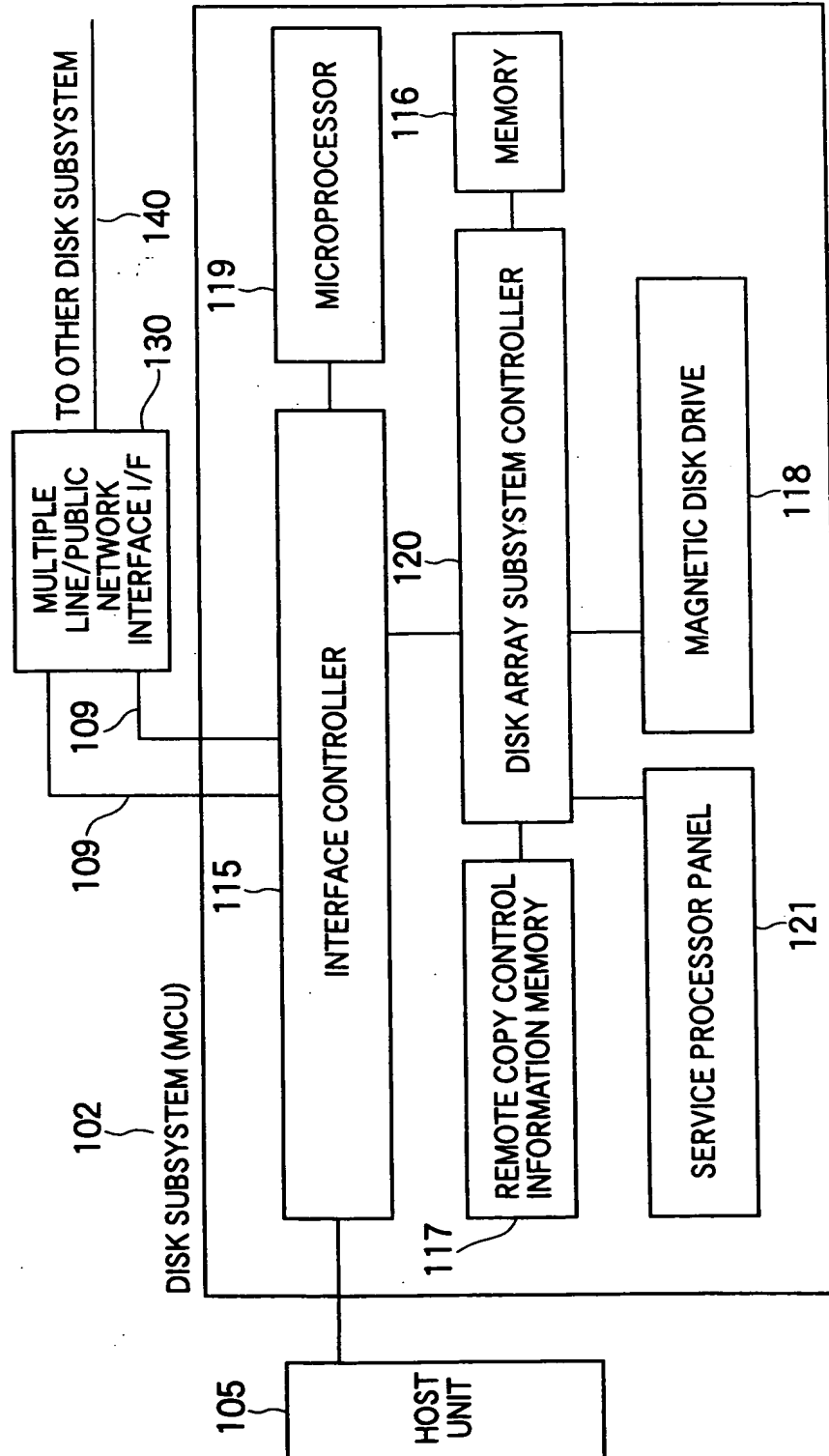
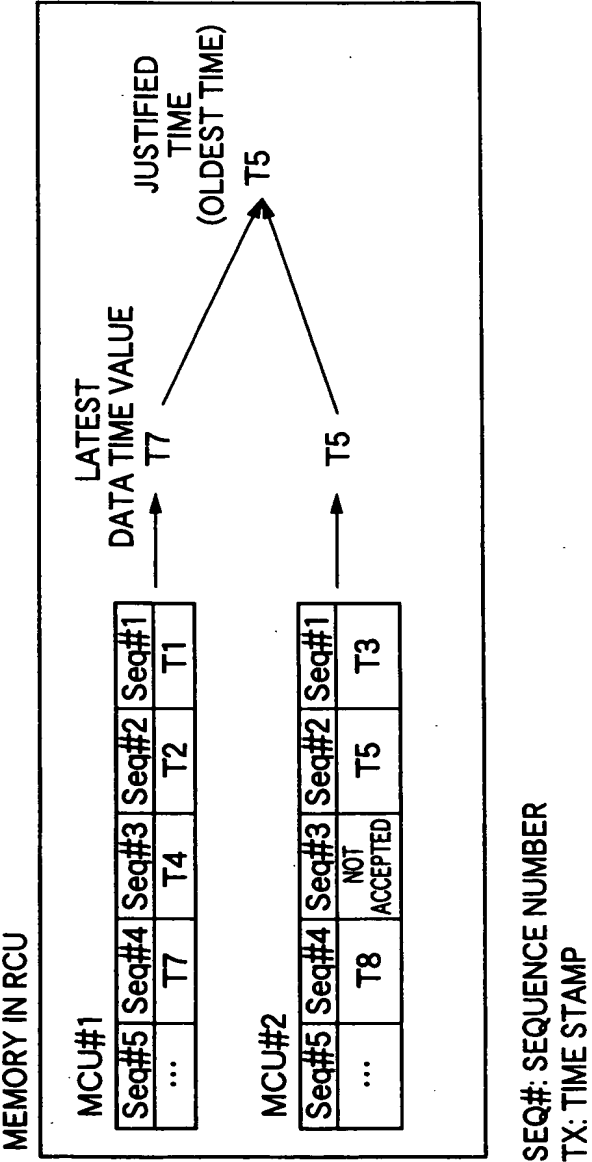
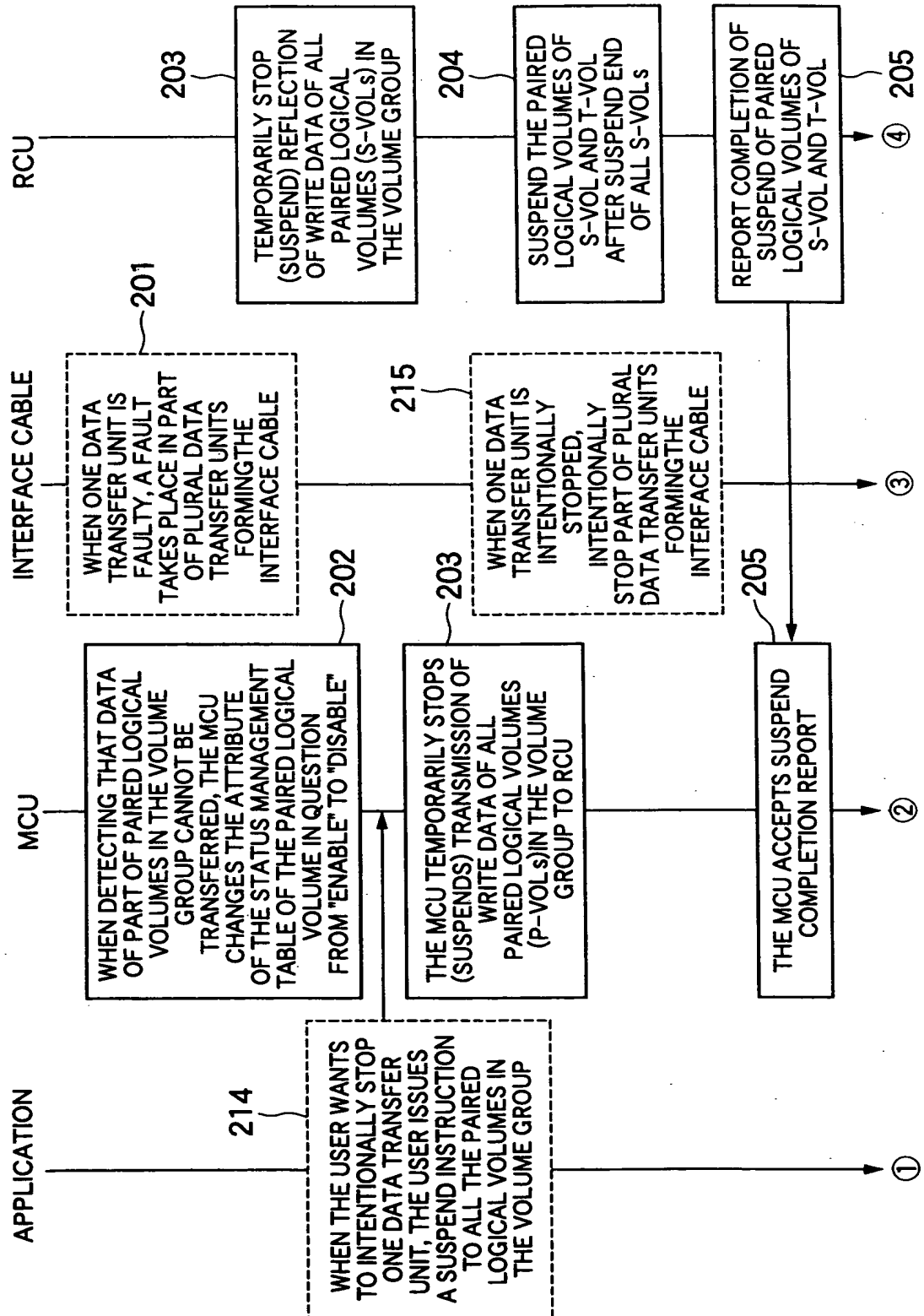


FIG. 3



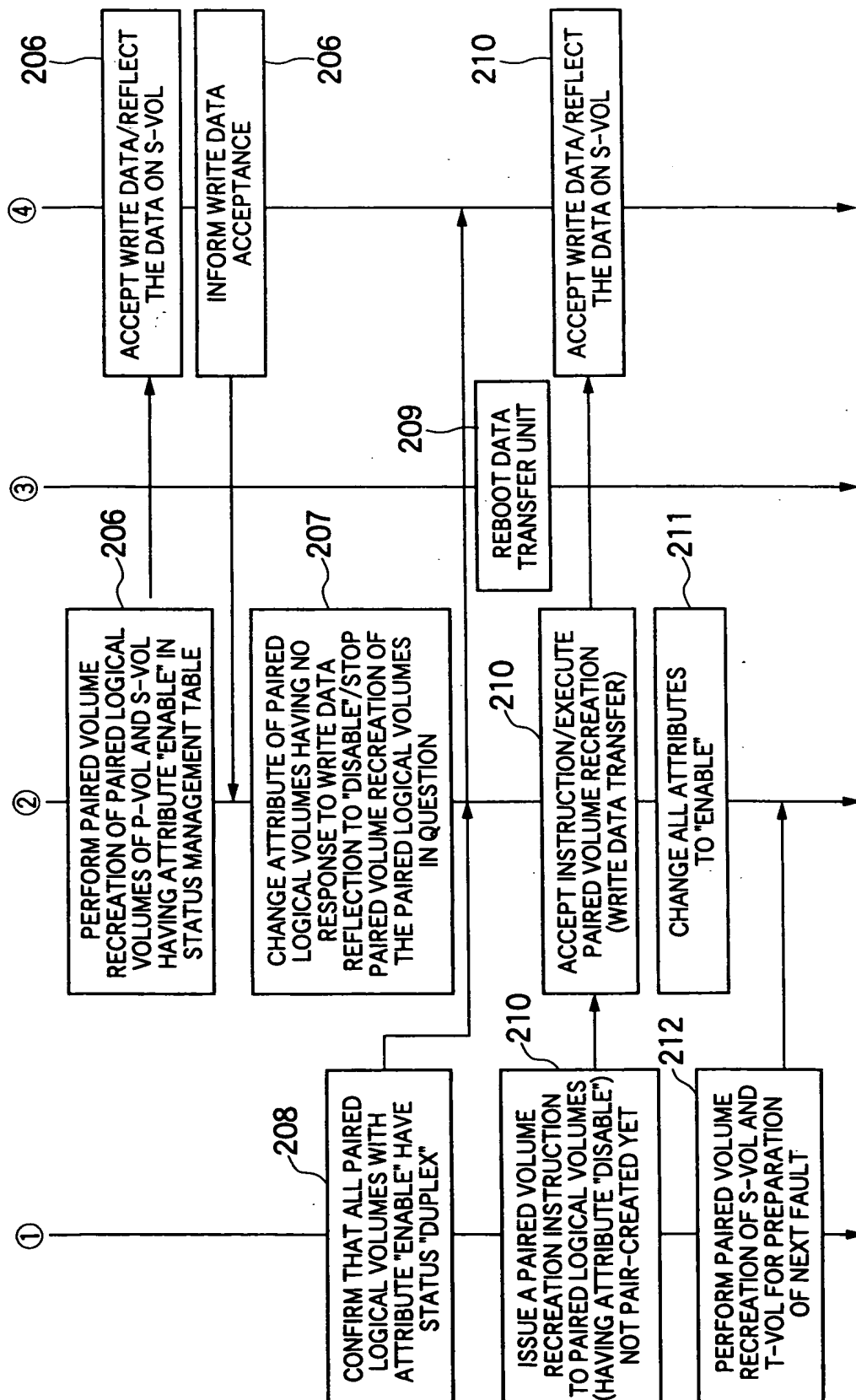
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FIG. 4



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

















FIG. 5



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Claim 3

FIG. 6



















DIVISION	STATUS TRANSITION OF PAIRED LOGICAL VOLUME			MCU STATUS MANAGEMENT TABLE					RCU STATUS MANAGEMENT TABLE	
	P-VOL	S-VOL	T-VOL	P-VOL	P-VOL STATUS	S- VOL	PAIRED VOLUME RECREATION ATTRIBUTE	S- VOL	S-VOL STATUS	
(1) <i>step 202</i>	 P-VOL DUPLEX	 S-VOL DUPLEX	 T-VOL DUPLEX	0-0:01	DUPLEX	1-0:01	ENABLE ↓ DISABLE	1-0:01	DUPLEX	
	 P-VOL DUPLEX	 S-VOL DUPLEX	 T-VOL DUPLEX	0-0:02	DUPLEX	1-0:02	¹ ENABLE	1-0:02	DUPLEX	
(2) <i>step 203</i>	 P-VOL SUSPEND	 S-VOL SUSPEND	 T-VOL DUPLEX	0-0:01	SUSPEND	1-0:01	¹ DISABLE	1-0:01	SUSPEND	
	 P-VOL SUSPEND	 S-VOL SUSPEND	 T-VOL DUPLEX	0-0:02	SUSPEND	1-0:02	ENABLE	1-0:02	SUSPEND	
(3) <i>step 204</i>	 P-VOL SUSPEND	 S-VOL SUSPEND	 T-VOL SUSPEND	0-0:01	SUSPEND	1-0:01	¹ DISABLE	1-0:01	SUSPEND	
	 P-VOL SUSPEND	 S-VOL SUSPEND	 T-VOL SUSPEND	0-0:02	SUSPEND	1-0:02	ENABLE	1-0:02	SUSPEND	
				601	602	603	604	605	606	

A

601 602 603 604 605 606

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FIG. 7

DIVISION	STATUS TRANSITION OF PAIRED LOGICAL VOLUME			MCU STATUS MANAGEMENT TABLE				RCU STATUS MANAGEMENT TABLE	
	P-VOL	S-VOL	T-VOL	P-VOL	P-VOL STATUS	S- VOL	PAIRED VOLUME RECREATION ATTRIBUTE	S- VOL	S-VOL STATUS
(4) <i>Step 206</i>				0-0:01	SUSPEND	1-0:01	DISABLE	1-0:01	SUSPEND
									
(5) <i>Step 40</i>				0-0:01	DUPLEX	1-0:01	DISABLE ↓ ENABLE	1-0:01	DUPLEX
									
(6) SAME AS (1)				0-0:01	DUPLEX	1-0:01	ENABLE	1-0:01	DUPLEX
									
				601	602	603	604	605	606

601 602 603 604 605 606

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FIG. 8

VG#=0001		JUSTIFIED TIME = 00:00:00	
P-VOL (MCU#-VOL#)	S-VOL (RCU#-VOL#)	STATUS	PAIRED VOLUME RECREATION ATTRIBUTE
0-0:01	1-0:01	DUPLEX	ENABLE
0-0:02	1-0:02	DUPLEX	ENABLE
0-0:03	1-0:03	SUSPEND	ENABLE
0-0:04	1-0:04	SUSPEND	DISABLE